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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Patent Application Serial No | |
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| Filing Date | |
| Inventor | |
| Assignee | Micron Technology, Inc. |
| Group Art Unit | |
| Examiner | |
| Attorney Docket No | Ml22-2379 |
| Title: Method and Apparatus for Testing S Method of Forming Apparatus for Test | emiconductor Circuitry for Operability and ting Semiconductor Circuitry for Operability |

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

- 1. Certificate of Facsimile Transmission
- 2. Supplemental Information Disclosure Statement
- 3. Form PTO-1449

Dated: June 14, 2004

By: Muriel B. Dunnigan

Telephone No. (509) 624-4276

NUMBER OF PAGES IN FACSIMILE: 4

FEE DEFICIENCY

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: _6-14-04

D. Brent Kenady

Telephone No. (509) 624-4276

Signature

PTO/SB/21 (02-04) Approved for use through 07/31/2006. OMB 0651-0031 U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number Application Number 10/632,273 TRANSMITTAL Filing Date July 31, 2003 FORM First Named Inventor Warren M. Farrworth Art Unit (to be used for all correspondence after initial filing) Examiner Name Unknown Attorney Docket Number 4 M122-2379 Total Number of Pages in This Submission **ENCLOSURES** (Check all that apply) After Allowance communication Fee Transmittal Form Drawing(s) to Technology Center (TC) Appeal Communication to Board Licensing-related Papers Fee Attached of Appeals and Interferences Appeal Communication to TC Pelition Amendment/Reply (Appeal Notice, Brief, Reply Brief) Pelition to Convert to a After Final Proprietary Information Provisional Application Power of Attorney, Revocation Status Letter Affidavits/declaration(s) Change of Correspondence Address Other Enclosure(s) (please Extension of Time Request **Terminal Disclaimer** Identify below): Form PTO-1449-List of Art Cited Request for Refund Express Abandonment Request Supplemental CD. Number of CD(s) Information Disclosure Statement Remarks Certified Copy of Priority Customer No. 021567 Document(s) Response to Missing Parts/ NO FEE IS REQUIRED WITH THIS FILING. However should a fee be Incomplete Application determined as owing, please charge such fee to Deposit Account No. 23-0925. Response to Missing Parts under 37 CFR 1,52 or 1,53 SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT D. Brent Kenady; Reg. No. 40,045 Wells St. John P.S. Individual name Signature Date CERTIFICATE OF TRANSMISSION/MAILING I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mall in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. Typed or printed name Muriel G. Dunnigan

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a banefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Tradamark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Date

June 14, 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Application Serial No. | | |
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| Filing Date | | July 31, 2003 |
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| Assignee | .,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,, | Micron Technology, Inc. |
| Group Art Unit | | |
| Examiner | | Unknown |
| | | |
| | paratus for Testing Semiconductor | |
| Method of Formir | ng Apparatus for Testing Semicond | ductor Circuitry for Operability |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 6-14-09

D. Brent Kenady Reg. No. 40,045

Customer No. 021567

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